



APL 001A

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Songjie Xu

Appl. No. : 09/754,406

Filed : January 2, 2001

For : METHODOLOGY AND
APPLICATIONS OF TIMING-
DRIVEN LOGIC
RESYNTHESIS FOR VLSI
CIRCUITS (as amended)

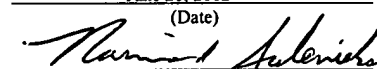
Examiner : Unknown

) Group Art Unit 2123

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) Arlington, VA 22202, on

) June 21, 2002

) (Date)

) 

) Raimond J. Salenieks, Reg. No. 37,924

PRELIMINARY AMENDMENT

United States Patent and Trademark Office
P.O. Box 2327
Arlington, VA 22202

Dear Sir:

In response to the Notice of Omitted Item(s) in a Nonprovisional Application dated February 20, 2001 in the above-referenced patent application, please make the following amendments:

IN THE SPECIFICATION:

Please replace the original specification with the substitute specification attached hereto.

06/28/2002 SHINASS1 00000007 09754406

01 FC:203
02 FC:202

99.00 OP
126.00 OP